RN.2 Technologies

Datasheet

ATS1020N20S

Rev. 1.4

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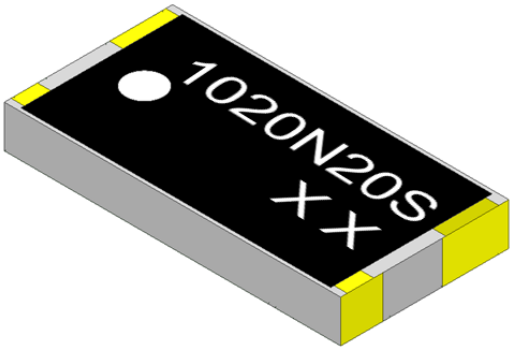
20dB, Attenuator
with High-Power Capacity and Stable Performance based on RN2 Resistor Technology
Model Name: ATS1020N20S

1. KEY FEATURES

- Frequency range DC ~ 5000 MHz
- Excellent high-power capacity up to average 30 watts
- Excellent stable performance at different temperatures
- Small size (2.54 x 5.08 mm) SMD package
- RoHS compliance (Pb-Free)



2. APPLICATIONS

- Applications using mobile networks, broadcast
- High power amplifiers
- Isolator, Circulator
- Military

3. GENERAL DESCRIPTIONS

ATS1020N20S is a 20dB attenuator with high-power capacity and stable performance in different temperatures. The AlN, high conductivity metal conductor (Ag), and gold (Au) plating enable ATS1020N20S to low return loss and improve durability for thermal stabilization and electricity.

ATS1020N20S is suited for applications using GSM, UMTS, LTE and 5G communications equipment, requiring high power.

ATS1020N20S supports up to average 30 watts. It is a SMD type product enabling Pb-Free solder and meets RoHS-6.

4. ELECTRICAL SPECIFICATIONS

Frequency (MHz)	Attenuation Value (dB)	Return Loss(S11) (dB, Min.)	Power Capacity Avg.(Watt)	Operating Temperature(℃)
DC ~ 4000	20 ± 1.0	20	30	-55 ~ 125
4000 ~ 5000	19.5 ± 1.5	20	30	-55 ~ 125

NOTE: These electrical specifications are measured by using a RN2 Technologies test board.
Specifications subject to change without notice.
For reliability test data, please contact RN2 Technologies sales team.

5. PORT CONFIGURATIONS

Figure 1 shows the locations of the ATS1020N20S ports. The orientation marker is included to represent input port.

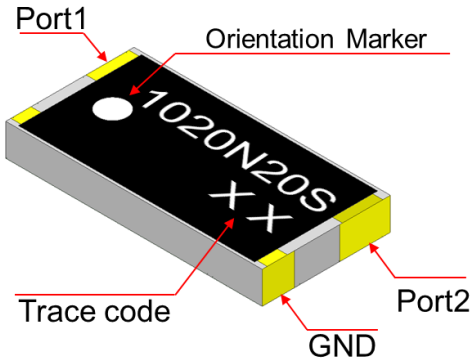
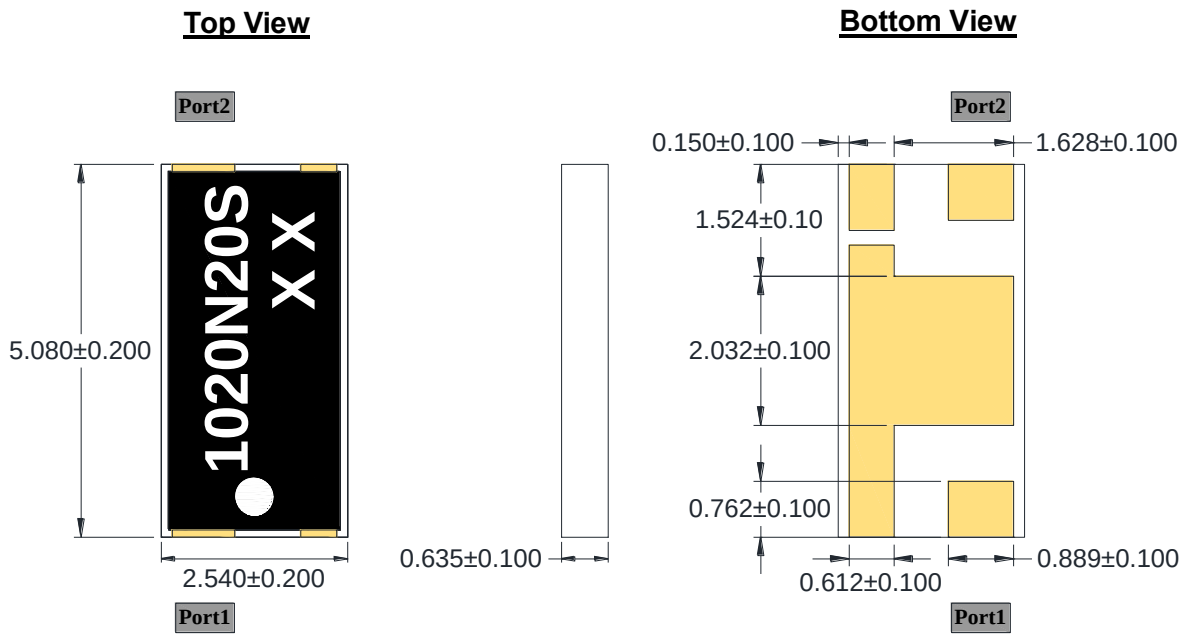


Figure 1. ATS1020N20S (Top View)

Table 1. ATS1020N20S Port Configurations

Configuration	Port 1	Port 2
Case 1.	Input	Output

6. OUT DIMENSION



- Unit: mm
- Weight: 0.03 grams
- Camber specifications: Less than 0.08 mm

7. POWER DERATING CURVE

The maximum allowable average power (Maximum input power, CW) of the ATS1020N20S depending on base PCB temperature changes. The maximum allowable average power of the ATS1020N20S is limited by the following power derating curve.

The ATS1020N20S factors that determine the power derating curve are as follows:

- Internal circuit
- Thickness
- Thermal conductivity of materials
- Operating temperature
- Mounting interface temperature between the ATS1020N20S and the base PCB

The maximum operating temperature of the ATS1020N20S is 100 ºC. Therefore, when the base PCB temperature is over 100 ºC, the ATS1020N20S operates stably by decreasing its durable average input power. When the base PCB temperature reaches 200 ºC, the maximum allowable average power decreases to 0 watt. .

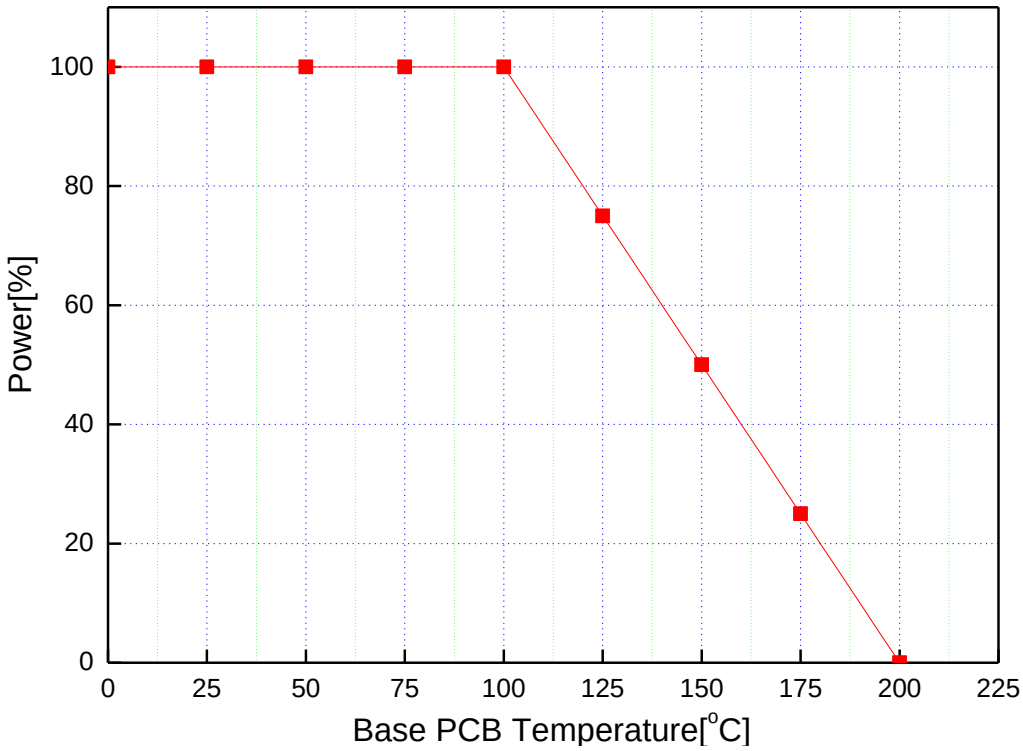


Figure 2. Power Derating Curve

8. RF Characteristics (at -55 ºC, 25 ºC and 125 ºC)

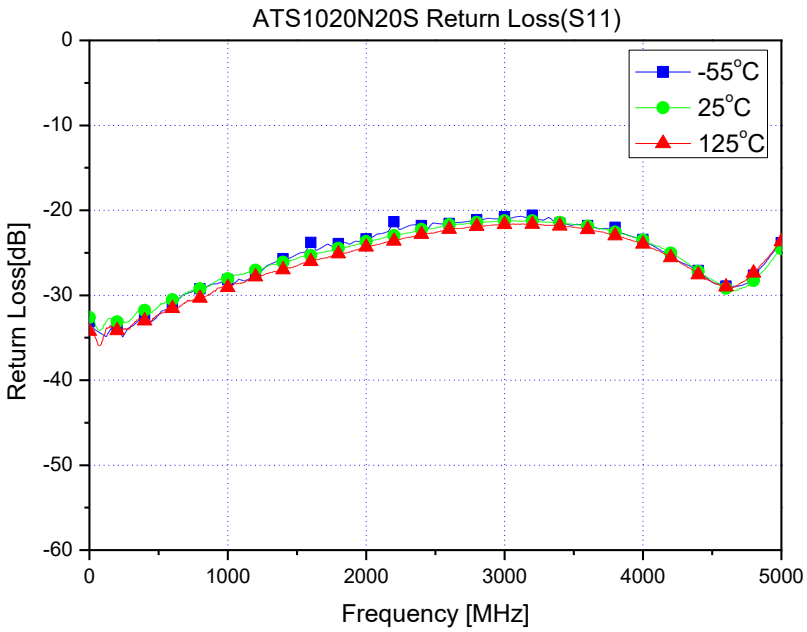


Figure 3. Test Plots of Return Loss (at -55 ºC, 25 ºC and 125 ºC)

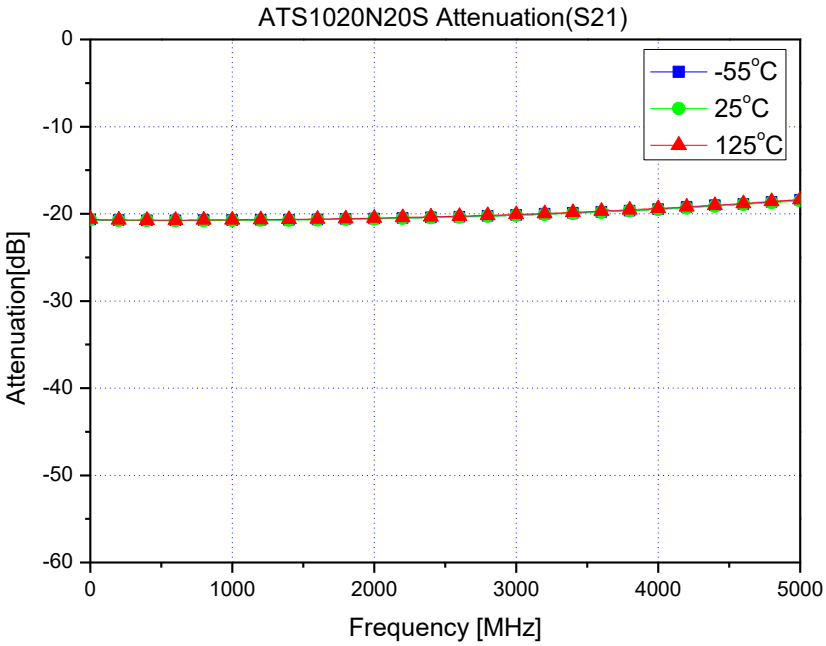


Figure 4. Test Plots of Attenuation (at -55 ºC, 25 ºC and 125 ºC)

9. RF TEST METHODS

To ensure S-Parameters reliability, we follow our internal test procedures by using the RN2 Technologies bare test board, RN2 Technologies test board, and test fixture connected with VNA (Vector Network Analyzer). In addition, we use the Automatic Port Extensions (APE) function of the Vector network analyzer to obtain accurate s-parameters.

9.1. RF TEST PROCEDURES

To test the ATS1020N20S RF performance, we perform the following steps:

1. Preparing the Test Equipment
2. Performing the APE Function of the VNA
3. Measuring the S-Parameters (Return Loss and Attenuation)
4. Obtaining the Characteristic Parameters (Return Loss and Attenuation)

STEP 1: Preparing the Test Equipment

The following test equipment is prepared to test the ATS1020N20S RF performance.

- RN2 Technologies bare test board
- RN2 Technologies test board
- Vector network analyzer
- Test fixture

NOTE: See *'RN2 Technologies TEST BOARD LAYOUT'* for the RN2 Technologies test board details.

STEP 2: Performing the Automatic Port Extensions (APE) Function of the Vector Network Analyzer

The APE function is used with the RN2 Technologies bare test board to correctly check the ATS1020N20S RF performance. This reduces or eliminates both electrical delay and insertion loss of the test fixture.

The detailed steps are as follows:

1. Place the RN2 Technologies bare test board on the test fixture.
2. Click the **Cal** button of the Vector network analyzer to calibrate it.
3. Connect the two ports of the test fixture into the two ports of the Vector network analyzer.
4. Click the **Port Extensions** button of the Vector network analyzer to measure the data of the RN2 Technologies bare test board.

NOTE: See *'AUTOMATIC PORT EXTENSIONS FUNCTION'* for more details.

STEP 3: Measuring the S-parameters (Return Loss and Attenuation)
After performing the APE function, the ATS1020N20S s-parameters are measured through the following steps:

1. Place the RN2 test board on the test fixture.
2. Apply pressure to the test fixture using a pneumatic piston.
3. Connect the two ports of the test fixture into the two ports of the Vector network analyzer.
4. Calibrate the Vector network analyzer.
5. Measure the return loss value from port 1 to port 1 (S11).
6. Measure the attenuation value from port 1 to port 2 (S21).

Figure 5. Test Setting to Measure the ATS1020N20S S-parameters

9.2. RN2 TEST BOARD LAYOUT

Figure 6 shows the RN2 Technologies test board layout used for testing the ATS1020N20S RF performance. The RN2 Technologies test board is based on the Taconic RF35 board with the dielectric constant of 3.5, board thickness of 0.8 mm, and copper of 1 Oz.

We recommend that you use the same material and design layout, as shown in ***Figure 6***, to meet the specifications in this datasheet. However, if you use different materials, you must follow the basic guidelines.

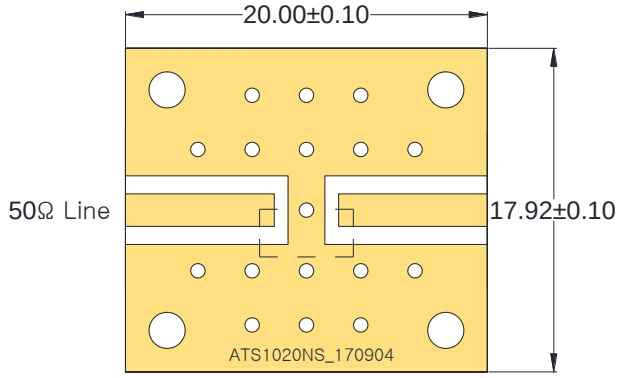


Figure 6. RN2 Technologies Test Board Layout

9.3. AUTOMATIC PORT EXTENSIONS (APE) FUNCTION

To accurately measure the ATS1020N20S s-parameters, we use the Automatic Port Extensions (APE) function of the Vector network analyzer. The APE function is used for reducing or eliminating both electrical delay and insertion loss of test fixtures. It provides a convenient, automated way to calculate the insertion loss and electrical delay terms by a simple measurement of an open or short circuit, which is easy to do in test fixtures.

We consider the transmission lines of the RN2 Technologies bare test board as extensions of the coaxial test cables that are between the Vector network analyzer and the ATS1020N20S. With the APE function, we extend the coaxial test ports so that our calibration plane is right at the terminals of the ATS1020N20S, and not at the connectors of the RN2 Technologies bare test board.

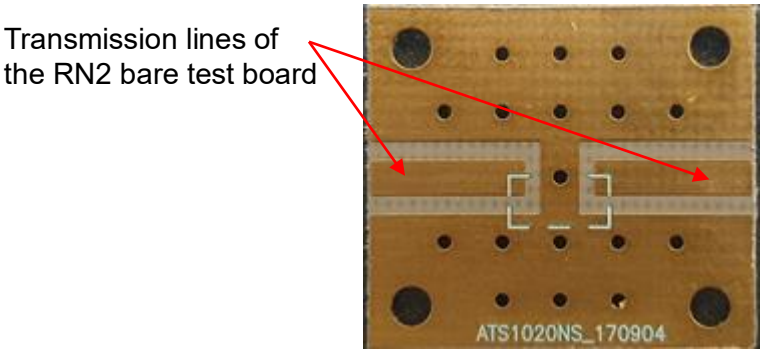


Figure 7. Performing the APE Function Test

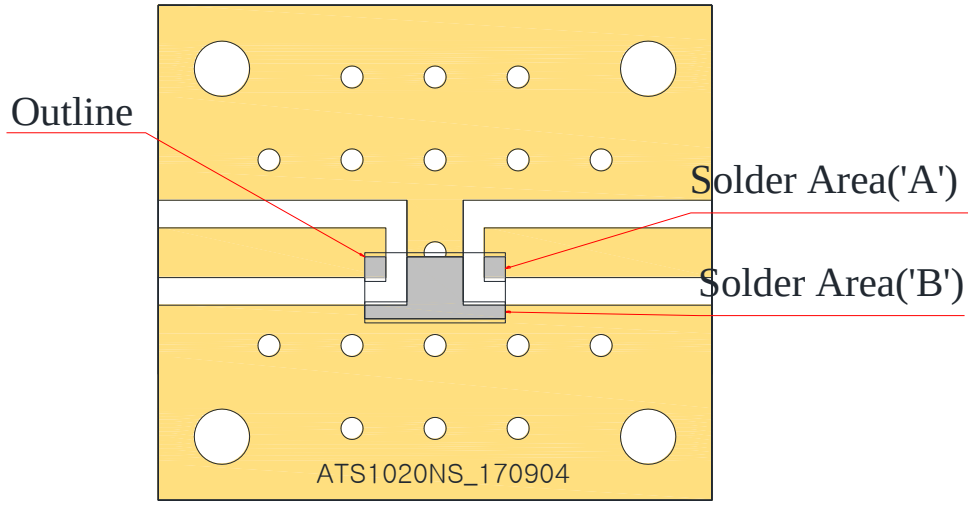
10. RECOMMENDED PCB LAYOUT

Figure 8 shows the recommended PCB layout to meet the specifications in this datasheet. When you use different materials other than the RN2 Technologies test board, you must follow the basic guidelines and calculate the 50 ohm impedance line width using a different PCB stack information.

Basic Guidelines

- Place GND more than 30% of the ATS1020N20S GND dimension regardless of a via size.
- Appropriately increase via sizes and numbers to allow low impedance ground connection and good thermal conductivity.
- Align the ATS1020N20S ground plane with the solder to have good connection to ground.
- Fill the via holes under the ATS1020N20S with the solder for thermal emission.

NOTE: Contact the RN2 Technologies sales team for more detailed PCB layout



NOTE

- Test Solder Cream: SAC-305 (Alpha Metal)
- Lead-Free Solder Alloy: Sn/Ag/Cu Ratio of 96.5/3.0/0.5
- Solder Area (‘A’) Dimension: 0.76 mm by 0.752 mm
- Solder Area (‘B’) Dimension: GND Land size

Figure 8. Recommended PCB Layout

11. SOLDERING PROCESS

The ATS1020N20S soldering steps are as follows:

1. Cleaning the PCB
2. Applying solder paste to the PCB
3. Placing the ATS1020N20S on the PCB
4. Reflowing the ATS1020N20S to the PCB
5. Cleaning and inspecting the soldered PCB with the ATS1020N20S

STEP 1: Cleaning the PCB
Carefully clean the PCB surface where the ATS1020N20S is soldered.
Particles must not be placed on the PCB surface where the ATS1020N20S is soldered.

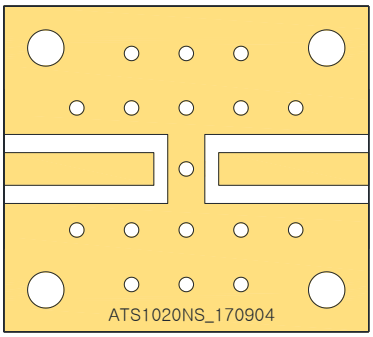


Figure 9. Cleaning the PCB Surface Where the ATS1020N20S is soldered

STEP 2: Applying the Solder Paste to the PCB
Apply the solder paste to the 3 points on the PCB surface.
It enables good thermal conductivity because the ATS1020N20S is firmly attached to the PCB surface without air.

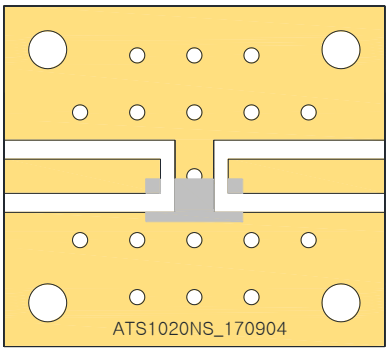


Figure 10. Applying the Solder Paste to the 3 points on the PCB Surface

STEP 3: Placing the ATS1020N20S on the PCB

Correctly place the ATS1020N20S on the 3 points of the PCB surface.
Applying the solder paste to the 3 points helps you firmly attach the ATS1020N20S to the PCB surface.

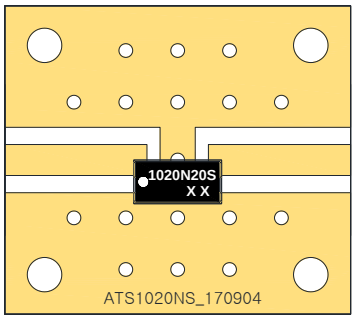


Figure 11. Placing the ATS1020N20S on the 3 points of the PCB Surface

STEP 4: Reflowing the ATS1020N20S to the PCB
We recommend both manual soldering and PCB surface pre-heating methods when reflowing the ATS1020N20S to the PCB surface. Be careful NOT to touch the iron tip to the ATS1020N20S when you use the manual soldering method.
See *'REFLOW PROFILE'* for more details.

12. REFLOW PROFILE

Figure 12 shows the thermal reflow profile of the SAC-305 (Alpha metal), which is a test solder cream we used.

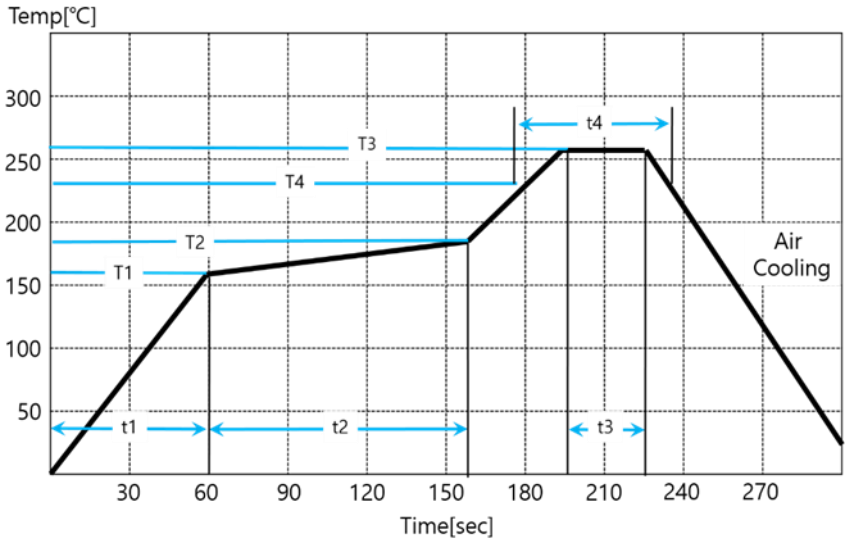
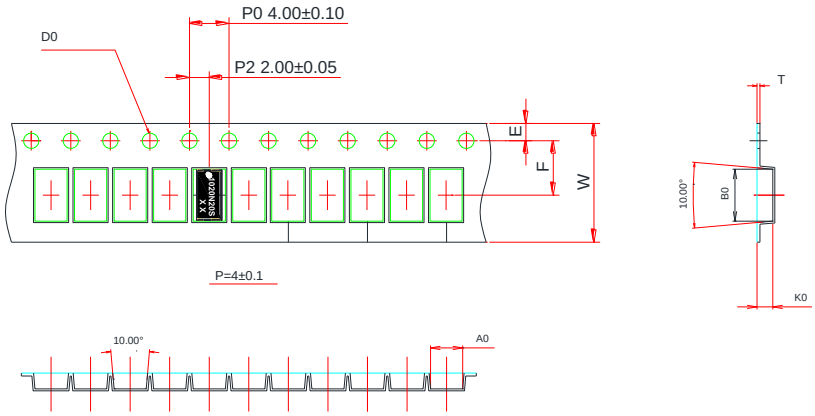
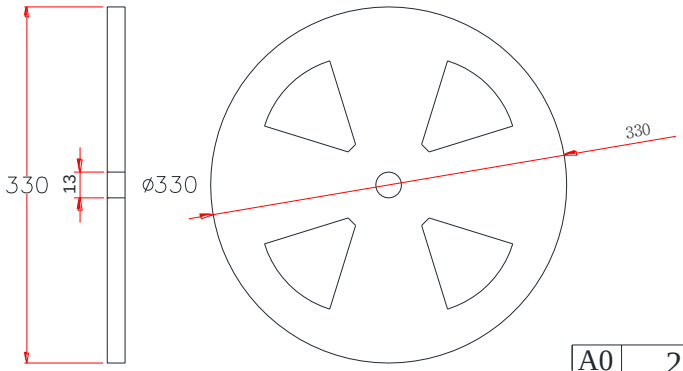


Figure 12. Thermal Reflow Profile

	Ramp Up	Pre-Heating	Peak	Soaking
Temperature(℃)	T1:160±5	T2:180±5	T3:260±5	T4:230±5
Time(sec)	t1:60±5	t2:100±15	t3:30±5	t4:60±10

13. PACKAGING AND ORDERING INFORMATION





A0	2.90±0.05	E	1.75±0.10
B0	5.45±0.05	F	5.50±0.05
D0	1.55±0.05	T	0.30±0.03
K0	1.78±0.05	W	12.00±0.30

Standard Packaging Quantity : 10,000 PCS / Reel

14. HANDLING GUIDE

PLEASE READ THIS NOTICE BEFORE USING OUR ATTENUATOR.

I. Be careful when transporting

- Ensure proper transportation as excessive stress or shock may damage Attenuator due to the nature of ceramics structure.
- Attenuator cracked or damaged on terminals may have their property changed.

II. Be careful during storage

- Store Attenuator less than temperature +30℃.
- Keep the humidity at 45% to 75% around Attenuator.
- Prevent corrosive gas (Cl_2, NH_3, SO_X, NO_X, etc.) from contacting Attenuator.
- It is recommended to use Attenuator within 6 months of receipt. If the period exceeds 6 months, solderability may need to be verified.

III. Be careful when soldering

- Solder all the ground terminals, IN and OUT pad of Attenuator on the ground plane of the PCB.
- Attenuator may be cracked or broken by uneven forces from a claw or suction device.
- Mechanical stress by any other devices may damage Attenuator when positioning them on PCB.
- Do not use dropped Attenuator.
- Ensure that any soldering is carried out by the condition of specification sheet.
- Do not re-use Attenuator which are de-soldered from PCB.

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